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2-4-02
M.L.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Poff et al.

Docket No.: IREA0002C

Serial Number: 09/886,167

Group Art Unit: 2171

Filed: 6 November 1997

Examiner: Unassigned

Title: HARDWARE ACCELERATOR FOR AN OBJECT-ORIENTED PROGRAMMING LANGUAGE

September 12, 2001

Commissioner of Patents and Trademarks
Washington, DC 20231

Preliminary Amendment

Sir:

Please add the following amendment and remarks.

Please charge the new claim fees to deposit account no. 07-1445.

In the Claims:

15. (new) An apparatus for accelerating a processor running an object-oriented programming language, comprising:

a hardware accelerator interfaced with said processor for implementing at least one application framework of said object-oriented programming language, wherein said at least one application framework comprises a set of classes that embodies an abstract design for solutions to a number of related problems; and

a means for a software stub that controls interfacing of said hardware accelerator with said processor.